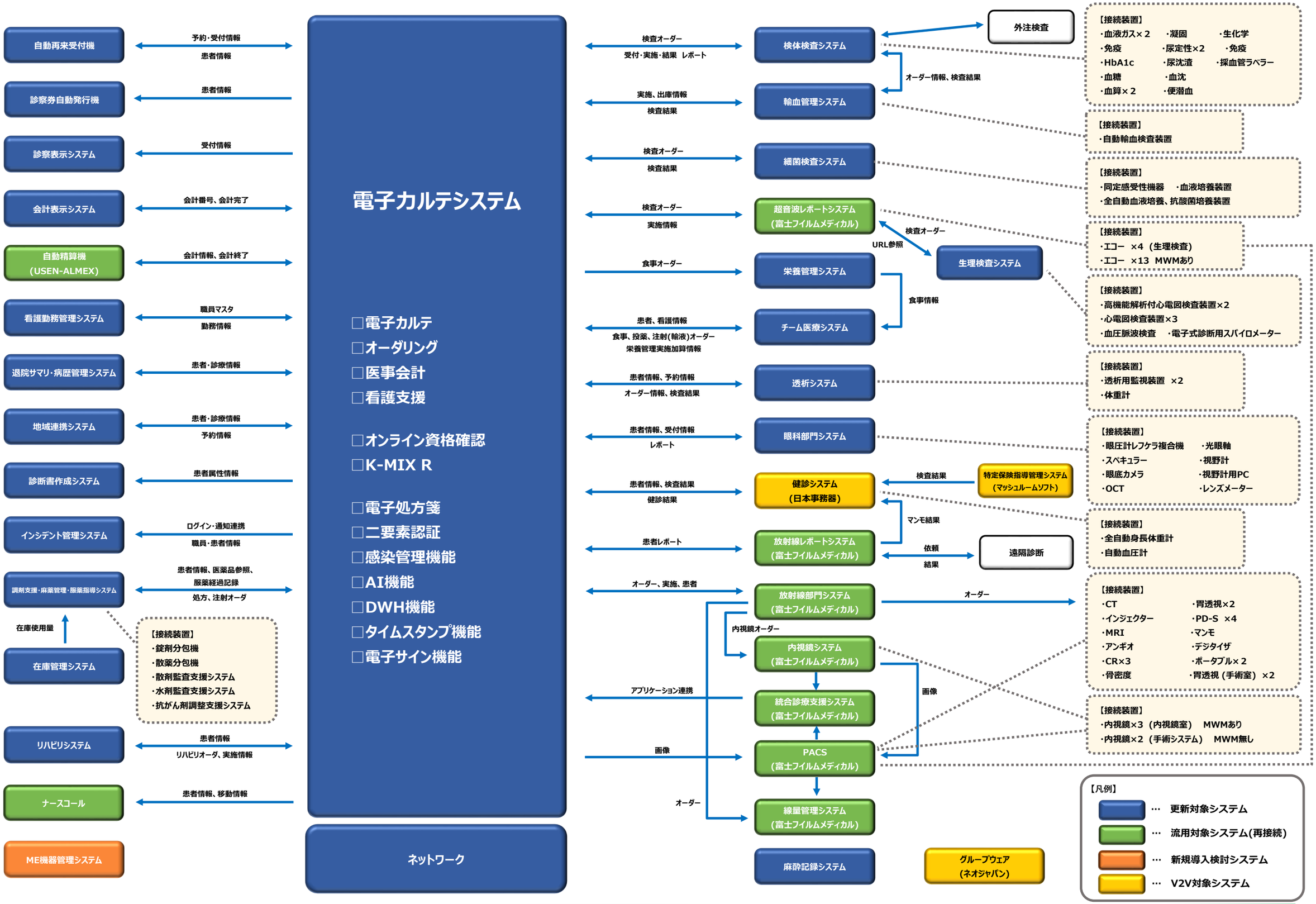
別紙2

次期システム構成図